


710	105
Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E. 12W SCANNED BK 	PATENT DATE
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APPLICATION NO. 09/661912	CONT/PRIOR D	CLASS 71 210	SUBCLASS 105	ART UNIT 2185	EXAMINER King Aaved
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APPLICANTS

Saeed Asimi

STUDY

High latency interface between hardware components

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. <div>5</div>	Figs. Drwg. <div>5</div>	Print Fig. <div>2</div>	Total Claims <div>75</div>	Print Claim for O.G. <div>1</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div>John Long</div> <div>(Assistant Examiner)</div> <div>7-9-13</div> <div>(Date)</div>			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	<div>_____</div> <div>(Primary Examiner)</div> <div>_____</div> <div>(Date)</div>			ISSUE FEE	
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